

Prior Art

FIG. 1



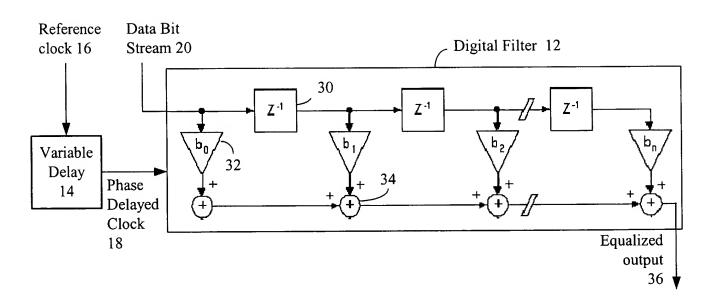
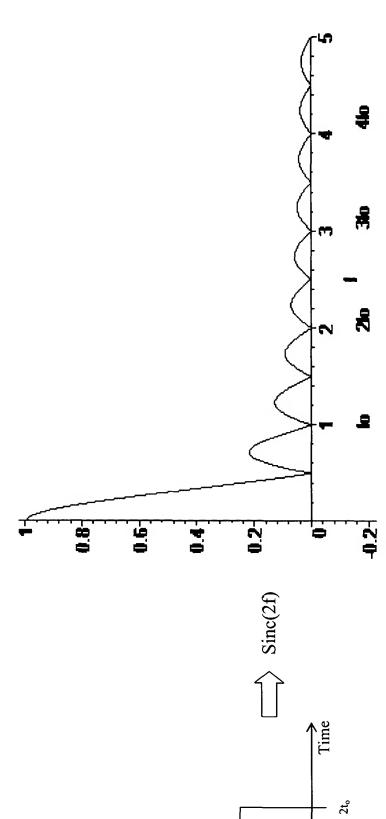


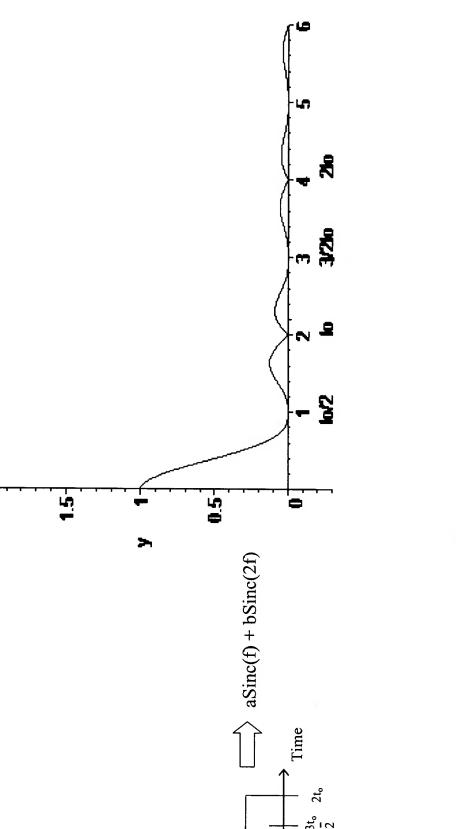
FIG. 2

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Scheme 1: No Equalization Prior Art

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Voltage

Prior Art Scheme 2: Bit Equalization

FIG. 5

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Scheme 3: Partial Bit Equalization

FIG. 6

